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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10014584	12/14/2001	257 438	314	2812 76	Mandalia

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**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2000-382396 12/15/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiner's initials		15.57/6348	
TITLE : Semiconductor devices having a non-volatile memory transistor and methods for manufacturing the same			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
TERMINAL		PREPARED FOR ISSUE	
DISCLAIMER		Application Examiner	
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